



Analog Behavioral Modeling with the Verilog-A Language

Dan FitzPatrick, Ira Miller

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Analog Behavioral Modeling With The Verilog-A Language provides the IC designer with an introduction to the methodologies and uses of analog behavioral modeling with the Verilog-A language. In doing so, an overview of Verilog-A language constructs as well as applications using the language are presented. In addition, the book is accompanied by the Verilog-A Explorer IDE (Integrated Development Environment), a limited capability Verilog-A enhanced SPICE simulator for further learning and experimentation with the Verilog-A language. This book assumes a basic level of understanding of the usage of SPICE-based analog simulation and the Verilog HDL language, although any programming language background and a little determination should suffice.

From the Foreword:

'Verilog-A is a new hardware design language (HDL) for analog circuit and systems design. Since the mid-eighties, Verilog HDL has been used extensively in the design and verification of digital systems. However, there have been no analogous high-level languages available for analog and mixed-signal circuits and systems.

Verilog-A provides a new dimension of design and simulation capability for analog electronic systems. Previously, analog simulation has been based upon the SPICE circuit simulator or some derivative of it. Digital simulation is primarily performed with a hardware description language such as Verilog, which is popular since it is easy to learn and use. Making Verilog more worthwhile is the fact that several tools exist in the industry that complement and extend Verilog's capabilities ...

Behavioral Modeling With the Verilog-A Language provides a good introduction and starting place for students and practicing engineers with interest in understanding this new level of simulation technology. This book contains numerous examples that enhance the text material and provide a helpful learning tool for the reader. The text and the simulation program included can be used for individual study or in a classroom environment ...'

Dr. Thomas A. DeMassa, Professor of Engineering, Arizona State University

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Ellen Jorge:

Analog Behavioral Modeling with the Verilog-A Language can be one of your beginning books that are good idea. All of us recommend that straight away because this guide has good vocabulary that may increase your knowledge in vocabulary, easy to understand, bit entertaining but delivering the information. The writer giving his/her effort to get every word into enjoyment arrangement in writing Analog Behavioral Modeling with the Verilog-A Language but doesn't forget the main position, giving the reader the hottest as well as based confirm resource info that maybe you can be one among it. This great information can drawn you into fresh stage of crucial contemplating.

Michael Herndon:

Within this era which is the greater particular person or who has ability in doing something more are more special than other. Do you want to become among it? It is just simple approach to have that. What you are related is just spending your time not much but quite enough to experience a look at some books. On the list of books in the top list in your reading list will be Analog Behavioral Modeling with the Verilog-A Language. This book and that is qualified as The Hungry Hillside can get you closer in turning into precious person. By looking up and review this book you can get many advantages.

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